

# Mitigating Latency in Chord-Based Routing under IPv6

Xubin Lin<sup>1</sup>, Feifei Hu<sup>1\*</sup>, and Liu Wu<sup>1</sup>

<sup>1</sup>Power Dispatching and Control Center, CSG, Guangzhou Guangdong, China

## Abstract

As the transition to IPv6 enables a massive expansion of the peer-to-peer (P2P) network landscape, traditional Chord-based routing protocols face significant performance bottlenecks due to the lack of awareness of the underlying physical network topology. In this paper, we propose eChord, a topology-aware routing system designed to mitigate latency in large-scale IPv6 environments by exploiting network locality. The key of eChord is a bifurcated routing architecture that maintains dual routing states: a *localFinger* table for intra-domain routing within autonomous systems (AS) and a *globalFinger* table for inter-domain connectivity. By optimizing the identifier space and introducing a locality factor  $\rho$  to prioritize local lookups, eChord can effectively reduce the reliance on high-latency backbone links. We then perform a multi-dimensional efficiency analysis of the eChord system, in terms of expected hop number, end-to-end latency, and state maintenance overhead. Numerical simulations are finally provided to demonstrate that the proposed eChord system can significantly outperform standard Chord in various network scales. In particular, in a network of  $10^6$  nodes distributed across 500 ASs with a locality factor of  $\rho = 0.8$ , the proposed eChord system reduces the average routing latency by approximately 75% compared to the traditional, locality-agnostic Chord protocol.

Received on 19 August 2025; accepted on 18 January 2026; published on 26 January 2026

**Keywords:** IPv6, latency, system design, performance evaluation

Copyright © 2026 Feifei Hu *et al.*, licensed to EAI. This is an open access article distributed under the terms of the [Creative Commons Attribution license](#), which permits unlimited use, distribution and reproduction in any medium so long as the original work is properly cited.

doi:10.4108/eetsis.9992

## 1. Introduction

IPv6 has been extensively studied as the fundamental solution to IPv4 address exhaustion, with a focus on architectural evolution and transition mechanisms [1–3]. To enable the coexistence of both protocols, three primary transition strategies have been proposed: dual-stack, tunneling, and translation [4, 5]. It is generally accepted that dual-stack architectures offer the most reliable native connectivity, whereas tunneling mechanisms, such as 6to4 and ISATAP, are utilized to encapsulate IPv6 packets over existing IPv4 infrastructure. The architectural design of the IPv6 protocol itself was optimized by simplifying the header structure to a fixed 40 bytes and eliminating the header checksum. This design decision was intended to reduce processing overhead at intermediate routers, thereby shifting fragmentation responsibilities to the source

node and streamlining the packet forwarding process within the network core [6–8].

The performance analysis and the comparisons between IPv4 and IPv6 have been conducted across various operating systems and network topologies to evaluate throughput, latency, and jitter [9–11]. It has been observed in empirical studies that native IPv6 networks often exhibit performance parity with IPv4, benefiting from the streamlined header processing despite the larger address size [12–14]. However, distinct performance degradation is frequently recorded when transition tunneling techniques are employed, as the encapsulation and decapsulation processes are found to introduce additional CPU overhead and increased round-trip time (RTT). Furthermore, maximum transmission unit (MTU) issues have been identified as a critical factor in performance bottlenecks, where the additional header overhead in tunneling scenarios often leads to packet fragmentation and reduced effective throughput [15–17].

\*Corresponding author. Email: [feifeihu2023@126.com](mailto:feifeihu2023@126.com)

In further, the system optimization on IPv6 networks has been performed as a critical area of research to address the requirements of modern mobility and quality of service (QoS) [18–20]. Standard mobile IPv6 (MIPv6) was initially found to suffer from high handover latency. Consequently, optimization protocols such as hierarchical mobile IPv6 (HMIPv6) and fast handovers (FMIPv6) were developed to localize signaling and minimize packet loss during network switching [21, 22]. Security optimization is also integrated into the system design through the mandatory support for IPsec, allowing authentication headers (AH) and encapsulating security payloads (ESP) to be processed end-to-end [23–25]. Additionally, network traffic optimization is facilitated by the flow label field in the IPv6 header, which is utilized to identify packet sequences requiring special handling, thereby ensuring that QoS parameters for real-time applications, such as VoIP and streaming media, are strictly maintained [26, 27].

This paper introduces **eChord**, a topology-aware routing system designed to minimize the end-to-end latency in large-scale IPv6 environments by leveraging network locality. The core of eChord is a bifurcated routing architecture that decouples routing states into two distinct structures: a *localFinger* table for intra-domain routing within autonomous systems (AS) and a *globalFinger* table for inter-domain connectivity. By refining the identifier space and introducing a locality factor  $\rho$  to prioritize local lookups, eChord significantly reduces dependence on high-latency backbone links. We then conduct a comprehensive efficiency analysis on the eChord system, evaluating key metrics including expected hop number, end-to-end latency, and state maintenance overhead. Numerical simulations are finally provided to demonstrate that eChord consistently outperforms the standard Chord protocol across diverse network scales. Specifically, in a network of  $10^6$  nodes distributed across 500 ASs with a locality factor of  $\rho = 0.8$ , the proposed system reduces average routing latency by approximately 75% compared to the traditional, locality-agnostic Chord protocol.

## 2. Proposed eChord System

In this section, we describe the architecture of the proposed eChord system, a hierarchical peer-to-peer routing framework optimized for the IPv6 address structure. Unlike the uniform identifier space of the standard Chord protocol, the proposed eChord system exploits network topology locality by bifurcating the logical ring into intra-domain and inter-domain layers.



### 2.1. Formalization of the Identifier Space

Let  $\mathcal{A}$  be the set of all valid 128-bit IPv6 addresses. We define a mapping function  $\mathcal{F} : \mathcal{A} \rightarrow \mathcal{I}$ , where  $\mathcal{I} = \{0, 1, \dots, 2^m - 1\}$  is the circular identifier space ( $m = 128$ ). For any node  $N_i$  with address  $A_i \in \mathcal{A}$ , the address is partitioned into a network prefix  $P_i$  of length  $L$  and an interface identifier  $H_i$  such that  $A_i = P_i \parallel H_i$ . The node identifier  $v_i \in \mathcal{I}$  is generated via a concatenated cryptographic hash:

$$v_i = \text{Hash}(P_i \parallel H_i) \pmod{2^m}. \quad (1)$$

This mapping ensures that nodes belonging to the same autonomous system, defined by the set  $\mathcal{C}_k = \{N_i \mid P_i = P_k\}$ , are logically grouped. We define the logical distance between two nodes  $v_i$  and  $v_j$  as:

$$d(v_i, v_j) = (v_j - v_i) \pmod{2^m}. \quad (2)$$

### 2.2. Dual-Tier Routing State Constraints

To optimize routing locality, each node  $v_i$  maintains a bifurcated routing state  $\mathcal{R}_i = \{LF_i, GF_i\}$ . The local table facilitates  $O(\log |\mathcal{C}_i|)$  routing within the same AS. The  $j$ -th entry of  $LF_i$  is defined as the first node that succeeds  $v_i$  by at least  $2^{j-1}$  within the local cluster  $\mathcal{C}_i$ :

$$LF_i[j] = \text{successor}(v_i + 2^{j-1}) \cap \mathcal{C}_i, \quad j \in \{1, \dots, m\}. \quad (3)$$

The global table maintains inter-AS connectivity. To minimize state redundancy, an identifier  $v_k$  is eligible for  $GF_i$  only if it resides in the "global gap" not covered by the local successor:

$$v_k \in GF_i \implies v_k \in \mathcal{I} \setminus [v_i, \text{Succ}(v_i)_{LF}]. \quad (4)$$

This constraint ensures that the global table only bridges long-range logical jumps that transcend the local administrative boundary.

### 2.3. Node Integration and SPNIS Logic

When a new node  $N_A$  joins the system, it should first resolve its Same Prefix Node Information Set (SPNIS). We define the SPNIS as the subset of active nodes sharing the longest common prefix match:

$$\text{SPNIS}(N_A) = \{N_i \in \text{System} \mid \text{PrefixMatch}(A_A, A_i) \geq L\}. \quad (5)$$

If  $\text{SPNIS} \neq \emptyset$ ,  $N_A$  selects a bootstrap node  $N_B$  by solving the following proximity optimization problem in the identifier space:

$$N_B = \arg \min_{N_i \in \text{SPNIS}} d(v_A, v_i). \quad (6)$$

By joining through  $N_B$ , the node  $N_A$  inherits a routing state that is already localized to its physical network segment, accelerating the convergence of  $LF_A$ .

## 2.4. Global Consistency and State Update

To maintain the global reachability of  $N_A$ , the system employs a delegated registration mechanism. Let  $\mathcal{H}_P = \text{Hash}(P_A)$  be the hash of the network prefix. The system identifies a supervisor node  $N_S$  such that:

$$v_S = \text{successor}(\mathcal{H}_P). \quad (7)$$

$N_A$  performs an atomic registration  $U$  to update the prefix directory at  $N_S$ :

$$U : \mathcal{M}_S \leftarrow \mathcal{M}_S \cup \{(v_A, \text{IP}_A)\}, \quad (8)$$

where  $\mathcal{M}_S$  is the metadata cache at node  $N_S$ . This formalizes the transition of  $N_A$  from an isolated node to a globally routable entity within the eChord hierarchy. Through this multi-tier registration, eChord maintains a global lookup complexity of  $O(\log N)$  while reducing the expected physical latency  $E[L]$  by a factor proportional to the local density  $|\mathcal{C}_i|/N$ .

## 3. Efficiency Analysis

In this section, we provide a rigorous theoretical analysis of the eChord system performance. We utilize a analytical model based on Chord's asymptotic properties, extending it to the hierarchical topology of eChord. We focus on three key metrics: routing path length (hops), end-to-end latency considering locality, and the storage overhead of routing tables.

Let  $N$  denote the total number of nodes in the network, and  $M$  denote the number of AS. We assume the nodes are uniformly distributed across domains, such that the average number of nodes per AS is  $N_{AS} = N/M$ .

### 3.1. Asymptotic Routing Complexity

In the standard Chord protocol, the routing process resolves a query in  $O(\log N)$  hops with high probability. eChord modifies this by splitting the routing state into local and global scopes. For a query where the target key resides within the same AS as the source node, eChord utilizes the *localFinger* table. Since the local network forms a Chord ring of size  $N_{AS}$ , the expected number of hops  $E[H_{local}]$  is governed by the logarithmic properties of the local ring:

$$E[H_{local}] = \frac{1}{2} \log_2(N_{AS}) = \frac{1}{2} \log_2\left(\frac{N}{M}\right). \quad (9)$$

This demonstrates that for localized traffic, eChord reduces the lookup complexity from  $O(\log N)$  to  $O(\log(N/M))$ , providing significant optimization when  $M$  is large.

For inter-domain queries, the node should route to the boundary of the local scope or utilize the *globalFinger* table. The global table effectively overlays

a coarser Chord ring over the entire identifier space. The expected number of hops for a global query,  $H_{global}$ , approximates the standard Chord behavior but is influenced by the density of the global fingers:

$$E[H_{global}] \approx O(\log N). \quad (10)$$

Although the hop numbers are asymptotically similar to standard Chord, the hierarchical lookup effectively decomposes the search space. The total path length  $H_{total}$  can be bounded by:

$$H_{total} \leq O(\log N_{AS}) + O(\log M). \quad (11)$$

This summation confirms that eChord maintains the scalability of the original protocol while favoring local segments.

### 3.2. Latency Analysis with Locality Awareness

Latency is the critical metric where eChord aims to differentiate itself. Unlike previous simplistic models, we introduce a locality factor  $\rho$  ( $0 \leq \rho \leq 1$ ), representing the probability that a query targets a node within the same AS. Let  $\delta_{intra}$  be the average transmission delay for an intra-domain hop, and  $\delta_{inter}$  be the delay for an inter-domain hop. Typically,  $\delta_{inter} \gg \delta_{intra}$  (e.g., 100ms vs 10ms). The expected end-to-end latency  $E[L]$  for eChord is calculated as the weighted sum of local and global query latencies:

$$E[L_{eChord}] = \rho \cdot (E[H_{local}] \cdot \delta_{intra}) + (1 - \rho) \cdot (E[H_{global\_path}] \cdot \bar{\delta}), \quad (12)$$

where  $\bar{\delta}$  represents the average link latency in a mixed path. Specifically, a global path consists of both internal forwarding and cross-domain jumps. We can approximate the global path latency as:

$$L_{global} \approx \alpha \cdot \delta_{intra} + \beta \cdot \delta_{inter}, \quad (13)$$

where  $\alpha + \beta = E[H_{global}]$ . Comparing this to the standard Chord, which is agnostic to locality:

$$E[L_{Chord}] = E[H_{Chord}] \cdot ((1 - P_{local\_link})\delta_{inter} + P_{local\_link}\delta_{intra}). \quad (14)$$

In standard Chord, the probability of a link being local ( $P_{local\_link}$ ) is roughly  $1/M$ , which is very small. Thus, standard Chord latency is dominated by  $\delta_{inter}$ .

Overall, the proposed eChord system minimizes the latency effectively when  $\rho \rightarrow 1$  (high data locality). However, if  $\rho \rightarrow 0$  and the network size  $N$  scales up, the overhead of maintaining the hierarchical structure may cause  $E[L_{eChord}]$  to exceed  $E[L_{Chord}]$ . Specifically, the potential path stretch caused by routing through specific gateways or sparse global fingers.

### 3.3. Storage and Maintenance Overhead

The efficiency of P2P systems is also defined by the state maintenance cost. While the standard Chord protocol requires a single finger table with a complexity of  $O(\log N)$ , eChord maintains two distinct tables to support its hierarchical topology: a *localFinger* table of size  $O(\log(N/M))$  and a *globalFinger* table of size  $O(\log N)$ . The total state size for eChord is:

$$S_{eChord} = O(\log \frac{N}{M}) + O(\log N) \approx O(\log N). \quad (15)$$

While the asymptotic storage complexity remains logarithmic, the constant factor is higher for eChord. This is because that as the network grows, the maintenance traffic for the dual-table structure consumes more bandwidth, potentially impacting the effective latency under high load conditions.

## 4. Simulations Results and Discussions

To validate the effectiveness of the proposed eChord protocol, we conduct a series of numerical simulations, focusing on the average routing latency across diverse network configurations. The simulation environment is structured as an IPv6 hierarchy consisting of  $M$  autonomous systems, where the total number of nodes  $N$  ranges from  $10^2$  to  $10^6$  to represent a large-scale peer-to-peer system. The network latencies are categorized into intra-domain delay  $\delta_{intra}$  and inter-domain delay  $\delta_{inter}$ , which are set to 10 ms and 100 ms by default, respectively, to simulate the inherent disparity between local and wide-area connections. The performance is assessed across varying degrees of data locality, represented by the factor  $\rho$ , which denotes the probability of a lookup target residing within the same AS as the initiator. The main parameters for the simulations in this paper are summarized in Table I.

Fig. 1 illustrates the expected end-to-end latency of the proposed eChord system versus the total network size  $N$ , ranging from  $10^2$  to  $10^6$  nodes, where there are 50 autonomous systems and the average intra-domain delay  $\delta_{intra}$  and inter-domain delay  $\delta_{inter}$  are set to 10 ms and 100 ms, respectively. From this figure, we can find that while the latency increases logarithmically with the total number of nodes  $N$  due to the inherent  $O(\log N)$  hop complexity of the underlying protocol, the proposed eChord system achieves substantial performance gains as the locality factor  $\rho$  increases. This is because of the eChord's bifurcated routing architecture, which maintains a dual routing state to exploit the network topology locality. A higher  $\rho$  signifies a larger probability that queries are resolved within the local AS, where the routing process utilizes the *localFinger* table. This localized routing effectively reduces the expected hop number and ensures that the packet transmission is governed

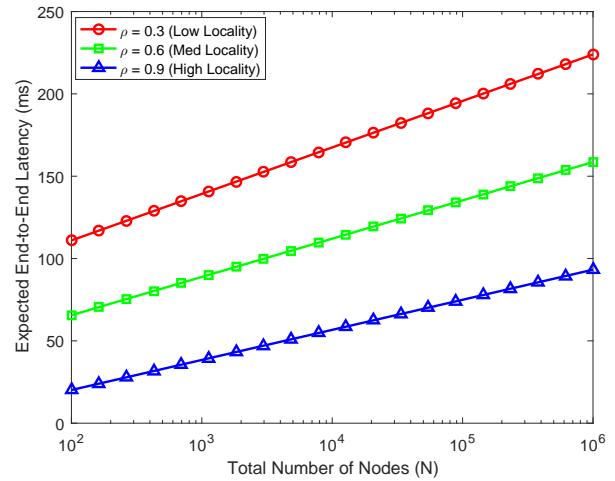


Figure 1. Latency of the proposed eChord system versus the total number of nodes  $N$ .

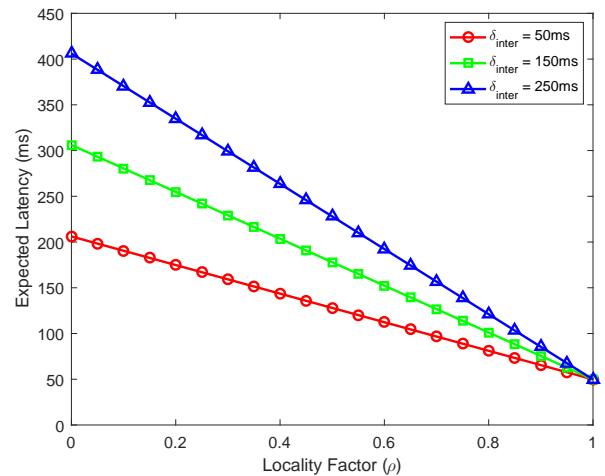


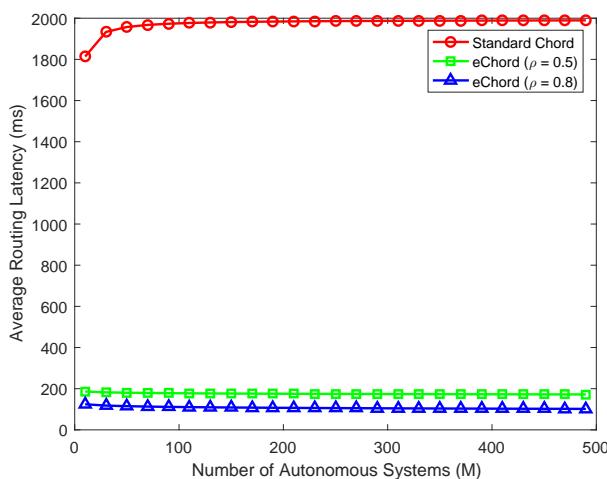
Figure 2. Latency of the proposed eChord system versus the locality factor  $\rho$ .

by the lower  $\delta_{intra}$  rather than the significantly higher  $\delta_{inter}$ , successfully mitigating the high latency typical of standard, locality-agnostic Chord protocols.

Fig. 2 illustrates the sensitivity of the eChord system's expected latency to the locality factor  $\rho$  versus the inter-domain delay, where the inter-domain delays  $\delta_{inter}$  is set to 50 ms, 150 ms, and 250 ms. Additionally, the number of nodes in the network is set to  $10^5$  and there are 100 autonomous systems, with the intra-domain delay  $\delta_{intra}$  set to 10 ms. From Fig. 2, we can see that the latency of the proposed eChord system becomes smaller as the locality factor  $\rho$  increases from 0 to 1. Notably, the performance gain is more pronounced in environments with higher  $\delta_{inter}$ , where the slope of the latency reduction is steeper. This is achieved due to the eChord's topology-aware

**Table 1.** Simulation Parameter Settings

Parameter (Symbol)	Value
Total number of nodes ( $N$ )	$10^2 - 10^6$
Number of autonomous systems ( $M$ )	50, 100, 10 ~ 500
Intra-domain delay ( $\delta_{intra}$ )	10 ms
Inter-domain delay ( $\delta_{inter}$ )	100 ms (or 50 ~ 250 ms)
Locality factor ( $\rho$ )	0 ~ 1.0 (e.g., 0.5, 0.8)
Identifier space length ( $L$ )	128 bits (IPv6)
Routing state structures ( $\mathcal{R}_i$ )	$\{LF_i, GF_i\}$

**Figure 3.** Latency comparison versus the number of autonomous systems  $M$ .

routing mechanism. In particular, a higher  $\rho$  implies that a larger proportion of lookups can be satisfied within the local AS using the *localFinger* table. This allows the system to “short-circuit” the global routing process, effectively substituting high-latency inter-domain hops with efficient intra-domain transmissions. By minimizing reliance on the  $\delta_{inter}$  component, the proposed eChord system can effectively mitigate the propagation delays that typically bottleneck the standard Chord performance in wide-area IPv6 networks.

Fig. 3 presents a comparative evaluation of the average routing latency between the standard Chord protocol and the proposed eChord system as the number of autonomous systems  $M$  scales from 10 to 500, where there are  $10^6$  nodes in the network and the intra-domain and inter-domain delays are fixed at 10 ms and 100 ms, respectively. From this figure, we can find a significant and widening performance gap between the two protocols as the network becomes more fragmented with an increasing  $M$ . Specifically, the standard Chord’s latency remains consistently high due to its locality and agnostic nature, where each routing hop has a high probability ( $1 - 1/M$ ) of

incurring a costly inter-domain delay. In contrast, the proposed eChord system achieves a dramatic reduction in the end-to-end latency, due to the following two reasons. One reason is that as  $M$  increases, the number of nodes per AS ( $N/M$ ) decreases, thereby reducing the expected local hop number  $E[H_{local}]$  for eChord. Another reason is that a higher locality factor  $\rho$  (e.g., 0.5 and 0.8) enables the proposed eChord system to resolve a majority of queries within the local domain using the *localFinger* table. By effectively “trapping” the search process within low-latency AS boundaries and bypassing the high-latency backbone links that bottleneck traditional Chord, the proposed eChord system achieves a superior scalability and efficiency in large-scale IPv6 environments.

## 5. Conclusions

This paper proposed a novel topology-aware routing system to minimize the end-to-end latency in large-scale IPv6 environments through the utilization of network locality. The proposed eChord system employed a bifurcated routing model that decoupled routing states into two separate structures: a *localFinger* table for intra-domain routing within autonomous systems and a *globalFinger* table for inter-domain connectivity. By refining the identifier space and incorporating a locality factor  $\rho$  to prioritize local lookups, the system substantially decreased reliance on high-latency backbone links. Thorough efficiency analyses were performed to assess key metrics, such as expected hop number, end-to-end latency, and state maintenance overhead. Numerical simulations showed that eChord consistently achieved better performance than the standard Chord protocol. Specifically, in a network of  $10^6$  nodes distributed across 500 ASs with a locality factor of  $\rho = 0.8$ , the proposed system reduced average routing latency by approximately 75% relative to the conventional, locality-agnostic Chord protocol.

### 5.1. acknowledgements

This work was supported by the Science and Technology Project of Southern Power Grid Corporation (No. ZDKJXM20230027).

## References

[1] Z. Wang, M. Goudarzi, and R. Buyya, "TF-DDRL: A transformer-enhanced distributed DRL technique for scheduling iot applications in edge and cloud computing environments," *IEEE Trans. Serv. Comput.*, vol. 18, no. 2, pp. 1039–1053, 2025.

[2] W. Jung, H. Park, M. Kim, H. Le, H. Jin, J. Hong, Y. Woo, and H. Lee, "A scalable distributed linear regulator with 99.5%-accuracy replica-based current sharing calibration for automotive applications," *IEEE Trans. Ind. Electron.*, vol. 72, no. 4, pp. 3633–3642, 2025.

[3] W. Li, Z. Fan, T. Liu, Z. Wang, H. Wu, M. Wu, K. Zhang, Y. Liu, N. Sun, X. Ye, and D. Fan, "DFU-E: A dataflow architecture for edge DSP and AI applications," *IEEE Trans. Parallel Distributed Syst.*, vol. 36, no. 6, pp. 1100–1114, 2025.

[4] C. Jing, X. Zhu, and X. Liu, "Performance analysis model and deterministic routing decision algorithm for broadband real-time services in wireless multi-hop networks," *IEEE Trans. Veh. Technol.*, vol. 72, no. 9, pp. 12113–12123, 2023.

[5] Z. Guo, C. Li, Y. Li, S. Dou, B. Zhang, and W. Wu, "Maintaining the network performance of software-defined wans with efficient critical routing," *IEEE Trans. Netw. Serv. Manag.*, vol. 21, no. 2, pp. 2240–2252, 2024.

[6] O. J. Pandey, T. Yuvaraj, J. K. Paul, H. H. Nguyen, K. Gundepudi, and M. K. Shukla, "Improving energy efficiency and qos of lpwans for iot using q-learning based data routing," *IEEE Trans. Cogn. Commun. Netw.*, vol. 8, no. 1, pp. 365–379, 2022.

[7] S. Luo, R. Cheng, B. Kao, X. Xiao, S. Zhou, and J. Hu, "ROAM: A fundamental routing query on road networks with efficiency," *IEEE Trans. Knowl. Data Eng.*, vol. 32, no. 8, pp. 1595–1609, 2020.

[8] A. Dalvandi, M. Gurusamy, and K. C. Chua, "Time-aware vmmflow placement, routing, and migration for power efficiency in data centers," *IEEE Trans. Netw. Serv. Manag.*, vol. 12, no. 3, pp. 349–362, 2015.

[9] Y. Jiao, L. Meng, Y. Li, Q. Yu, and Y. Gu, "Efficient change-point detection over fully decentralized wireless networks with low communication rate," *IEEE Trans. Veh. Technol.*, vol. 74, no. 1, pp. 1626–1642, 2025.

[10] H. Wang and Y. Chi, "Communication-efficient federated optimization over semi-decentralized networks," *IEEE Trans. Signal Inf. Process. over Networks*, vol. 11, pp. 147–160, 2025.

[11] Y. Lu, S. Zhao, Y. Zang, Z. Bian, and Y. Zheng, "Spectral-adaptive consensus algorithm for robust fault mitigation in decentralized smart manufacturing networks," *IEEE Trans. Cybern.*, vol. 55, no. 8, pp. 3987–4000, 2025.

[12] M. Alghamdi, L. He, S. Ren, and M. Maray, "Efficient parallel processing of all-pairs shortest paths on multicore and GPU systems," *IEEE Trans. Consumer Electron.*, vol. 70, no. 1, pp. 2896–2908, 2024.

[13] X. Zhou, K. Huang, L. Li, M. Zhang, and X. Zhou, "I/o-efficient multi-criteria shortest paths query processing on large graphs," *IEEE Trans. Knowl. Data Eng.*, vol. 36, no. 11, pp. 6430–6446, 2024.

[14] N. Ganganath, C. Cheng, and C. K. Tse, "A constraint-aware heuristic path planner for finding energy-efficient paths on uneven terrains," *IEEE Trans. Ind. Informatics*, vol. 11, no. 3, pp. 601–611, 2015.

[15] X. Zhang, J. Chen, L. Su, G. Gong, and F. Zhang, "Active fault tolerance for sensor failures in steer-by-wire systems via multi-model adaptive kalman filter," *IEEE Trans. Veh. Technol.*, vol. 74, no. 4, pp. 5442–5452, 2025.

[16] J. Yu, J. Yang, Q. Li, Y. Pan, C. Gao, and S. Huang, "A two-terminal hybrid parallel connection method for simultaneously enhancing the output performance and fault tolerance of dual three-phase machines," *IEEE Trans. Ind. Electron.*, vol. 72, no. 5, pp. 4567–4576, 2025.

[17] B. Li, Z. Li, J. Zong, H. Wang, N. Li, and H. Li, "A novel proactive fault tolerance loss function for crack segmentation," *IEEE Trans. Intell. Transp. Syst.*, vol. 26, no. 5, pp. 6361–6378, 2025.

[18] Y. Yao, Z. Zhu, P. Miao, X. Cheng, F. Shu, and J. Wang, "Optimizing hybrid ris-aided ISAC systems in V2X networks: A deep reinforcement learning method for anti-eavesdropping techniques," *IEEE Trans. Veh. Technol.*, vol. 74, no. 6, pp. 9224–9239, 2025.

[19] N. Lin, Z. Wang, L. Zhao, A. Hawbani, Z. Liu, and M. Guizani, "Optimizing multi-aav cooperative tracking for real-time applications in network-challenged environments," *IEEE Trans. Computers*, vol. 74, no. 7, pp. 2461–2472, 2025.

[20] X. Wang, J. Lv, B. Kim, B. D. Parameshachari, K. Li, D. Yang, and A. Shankar, "Optimizing deep neuro-fuzzy network for ECG medical big data through integration of multiscale features," *IEEE Trans. Fuzzy Syst.*, vol. 33, no. 7, pp. 2027–2037, 2025.

[21] Y. Feng, J. Gao, and C. Xu, "Learning dual-routing capsule graph neural network for few-shot video classification," *IEEE Trans. Multim.*, vol. 25, pp. 3204–3216, 2023.

[22] H. Huang, H. Yin, G. Min, J. Zhang, Y. Wu, and X. Zhang, "Energy-aware dual-path geographic routing to bypass routing holes in wireless sensor networks," *IEEE Trans. Mob. Comput.*, vol. 17, no. 6, pp. 1339–1352, 2018.

[23] K. Zhang, X. Wang, B. Yi, M. Huang, L. Qiu, E. Lv, and J. Guo, "A reliable distributed-cloud storage based on permissioned blockchain," *IEEE Trans. Serv. Comput.*, vol. 18, no. 3, pp. 1216–1231, 2025.

[24] Y. Chen, Y. Li, Y. Lu, Z. Pan, Y. Chen, S. Ji, Y. Chen, Y. Li, and Y. Shen, "Understanding the security risks of websites using cloud storage for direct user file uploads," *IEEE Trans. Inf. Forensics Secur.*, vol. 20, pp. 2677–2692, 2025.

[25] Q. Zhang, S. Qian, J. Cui, H. Zhong, F. Wang, and D. He, "Blockchain-based privacy-preserving deduplication and integrity auditing in cloud storage," *IEEE Trans. Computers*, vol. 74, no. 5, pp. 1717–1729, 2025.

[26] J. Liu, Y. Wu, S. Su, and X. Wang, "Distributed photovoltaic system anomaly detection based on metering data mining analysis," *Southern Power System Technology*, vol. 18, no. 12, pp. 117–126, 2024.

[27] Y. Xu, T. Ji, and M. Li, "Day-ahead and intra-day coordinated optimal scheduling of microgrid based on

deep reinforcement learning,” *Southern Power System Technology*, vol. 18, no. 9, pp. 106–116, 2024.